



Attorney's Docket No. 02282.P055

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Raynaud et al.

Application No.: 09/127,584

Filed: July 31, 1998

For: **Method and Apparatus for Gate-Level
Simulation of synthesized Register
Transfer Level Designs with Source-
Level Debugging**

) Group Art Unit: 2764

) Examiner: Not yet Assigned

) I hereby certify that this correspondence is being
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Assistant Commissioner for Patents
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RESPONSE TO NOTICE TO FILE MISSING PARTS OF APPLICATION
(FILING DATE GRANTED)

Sir:

In response to the Notice to File Missing parts of Application (Filing Date Granted)
mailed August 28, 1998, please find enclosed:

- (1) a duly executed Declaration and Power of Attorney with respect to the
above-referenced patent application;
- (2) a check in the amount of \$130.00 in payment of the surcharge of 37 C.F.R. §
1.16(e); and
- (3) a copy of the Notice to File Missing Parts of Application;

If any additional fee is required, please charge Deposit Account No. 02-2666. A
duplicate of this Response is enclosed for deposit account charging purposes.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 9/15/98

Aloysius T.C. AuYeung
Reg. No. 35,432

12400 Wilshire Blvd.
Seventh Floor
Los Angeles, CA 90025-1026
(503) 684-6200